

REVISION HISTORY

Schematics Index:

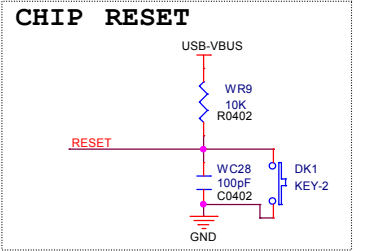
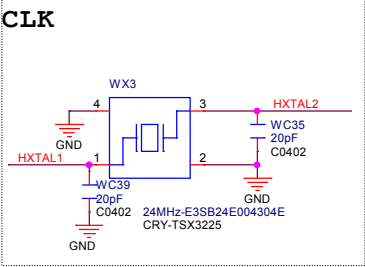
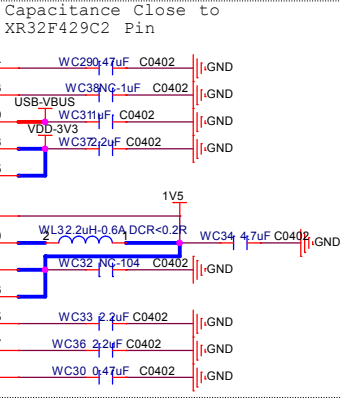
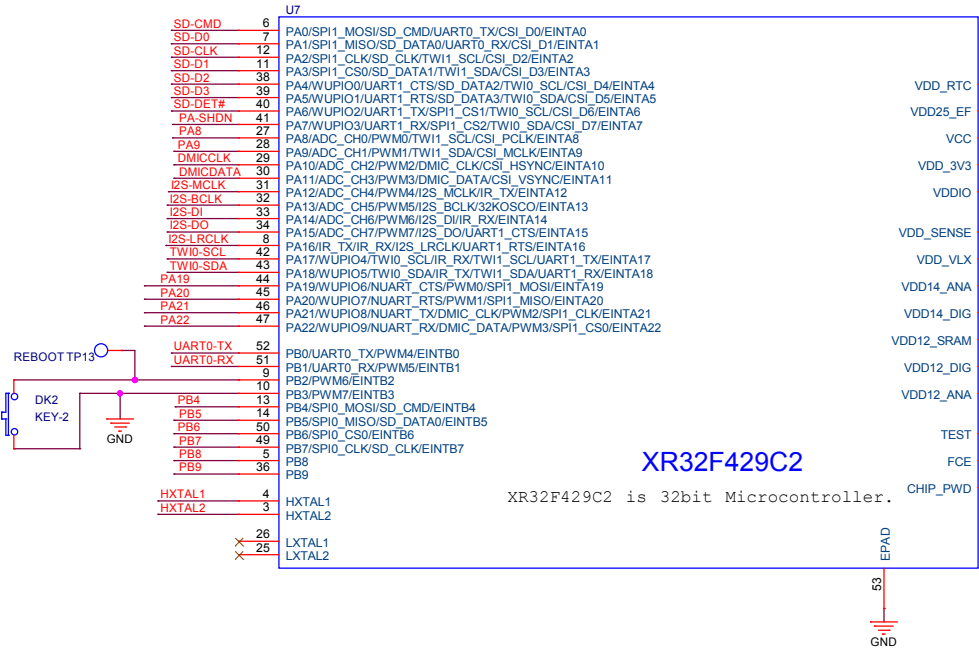
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Revision	Description	Date	Drawn
Ver 1.0	draft	2018-10-23	dingjinlong

XR32

USB-VBUS
Input

VDD-3V3
Output



06	SD-D2	<<<	SD-D2
06	SD-D3	<<<	SD-D3
06	SD-CMD	<<<	SD-CMD

06	SD-CLK	<<<	SD-CLK
06	SD-D0	<<<	SD-D0
06	SD-D1	<<<	SD-D1
06	SD-DET#	<<<	SD-DET#

06	PA8	<<<	PA8
06	PA9	<<<	PA9

06	PB4	<<<	PB4
06	PB5	<<<	PB5
06	PB6	<<<	PB6
06	PB7	<<<	PB7
06	PA19	<<<	PA19
06	PA21	<<<	PA21
06	PA22	<<<	PA22

PA20 R95 0R R0402 JDET <<< JDET 06

05 PA-SHDN <<< PA-SHDN

05 I2S-MCLK <<< I2S-MCLK

05 I2S-BCLK <<< I2S-BCLK

05 I2S-LRCLK <<< I2S-LRCLK

05 I2S-DI <<< I2S-DI

05 I2S-DO <<< I2S-DO

05 TWI0-SCL <<< TWI0-SCL

05 TWI0-SDA <<< TWI0-SDA

05 PB8 <<< PB8

05 PB9 <<< PB9

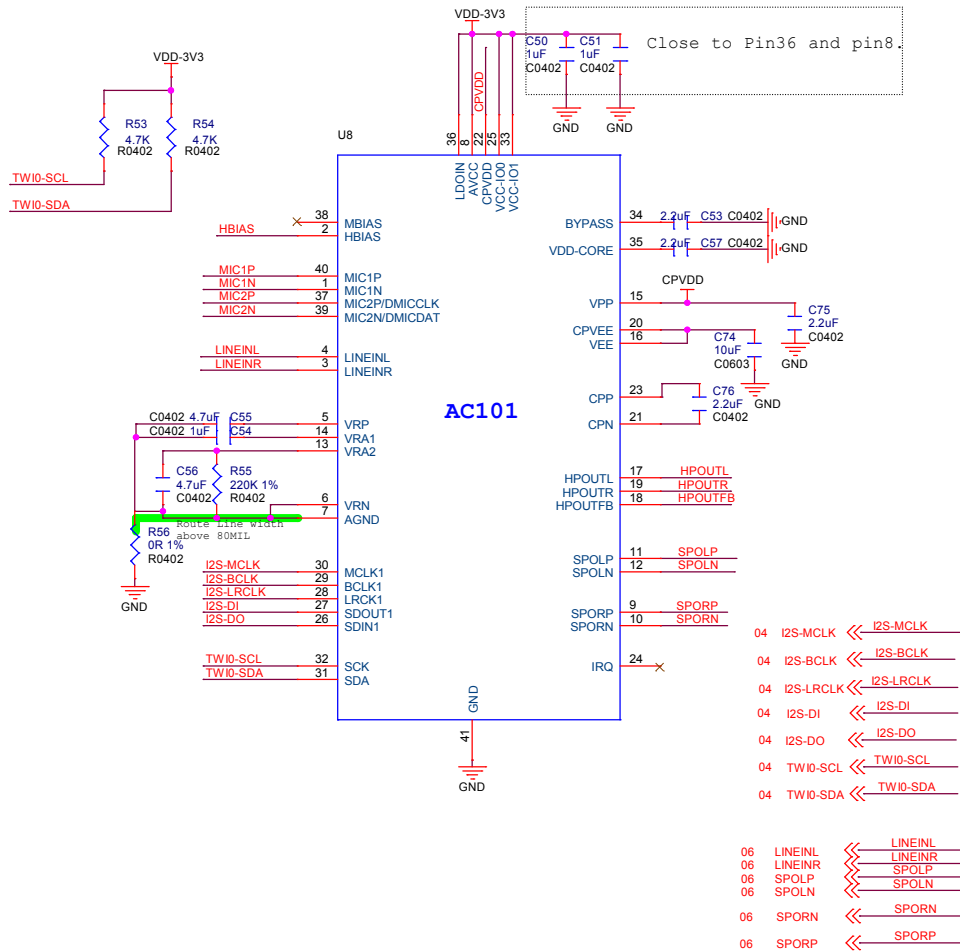
05 UART0-TX <<< UART0-TX

05 UART0-RX <<< UART0-RX

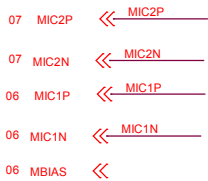
04 DMICCLK <<< DMICCLK

04 DMICDATA <<< DMICDATA

AC101



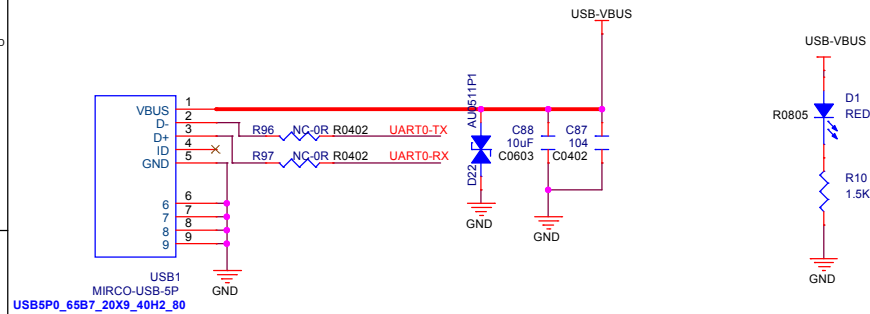
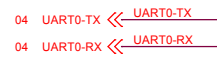
ECM MIC



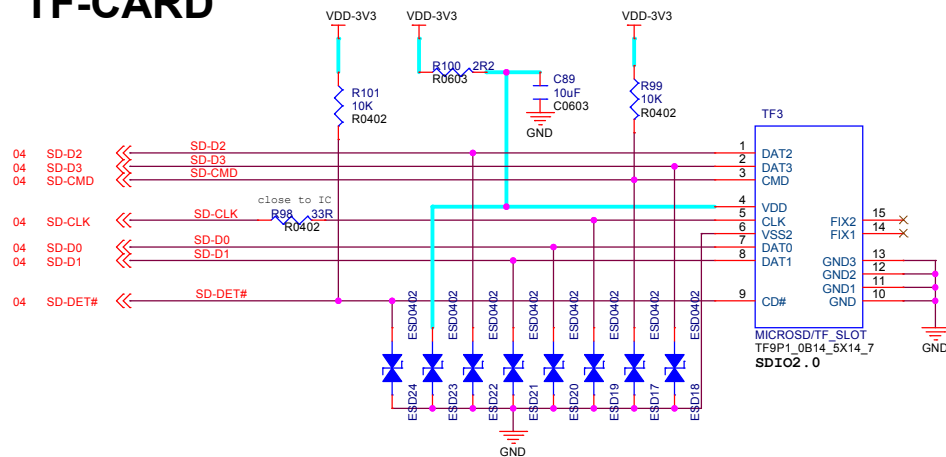
Close to AC101



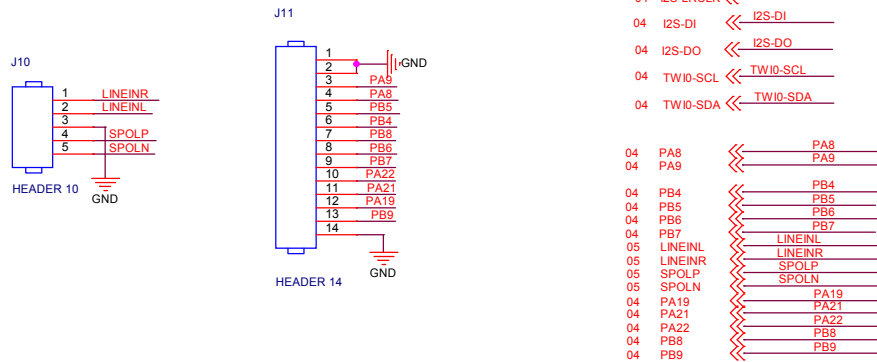
POWER



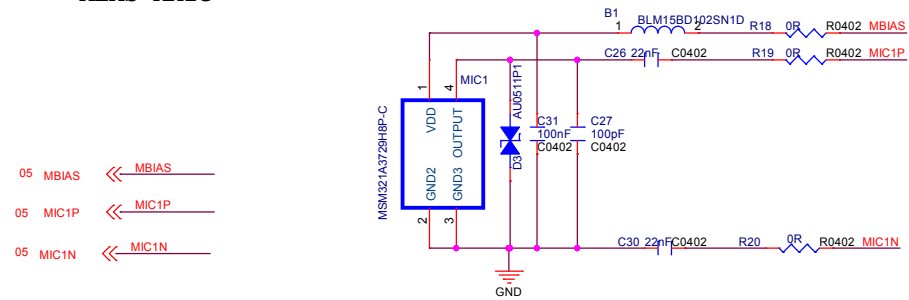
TF-CARD



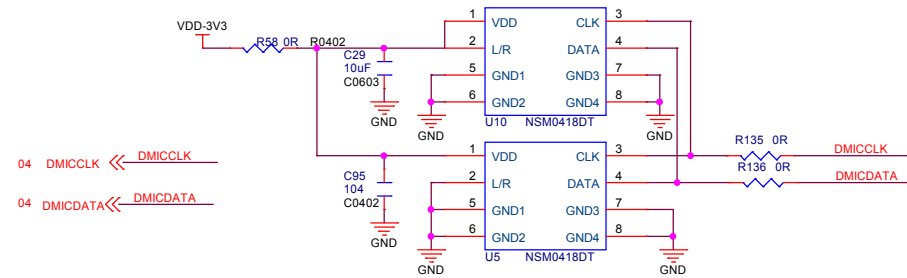
INTERFACE



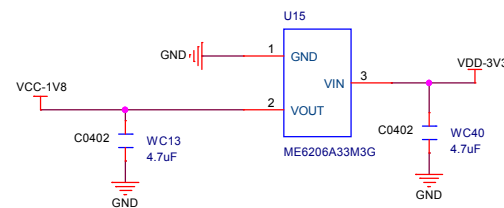
MEMS AMIC



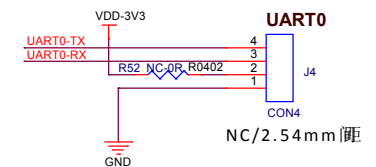
MEMS DMIC



VCC-1V8 (Option)

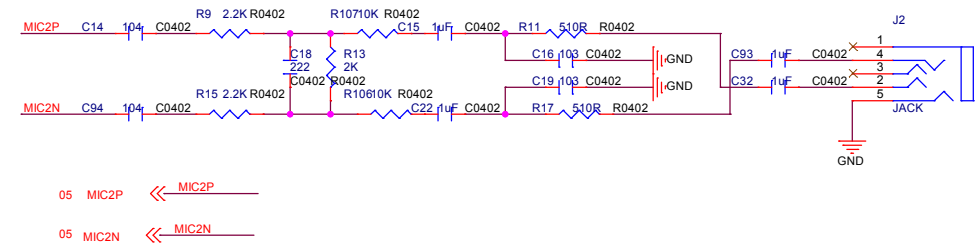


DEBUG

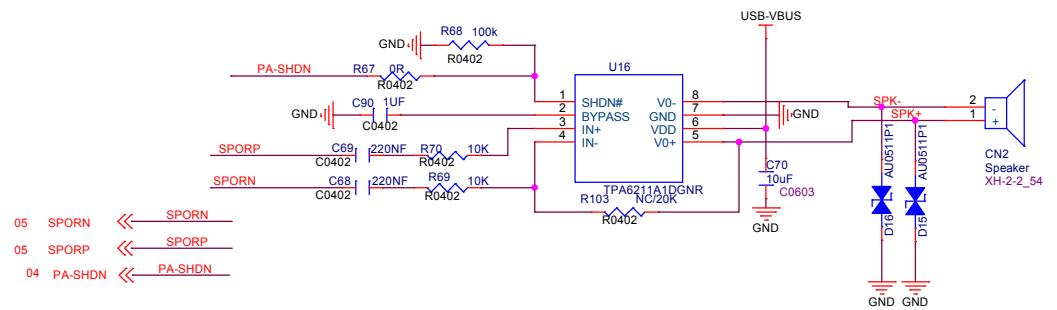


LINE-IN

Line_in back to MIC2

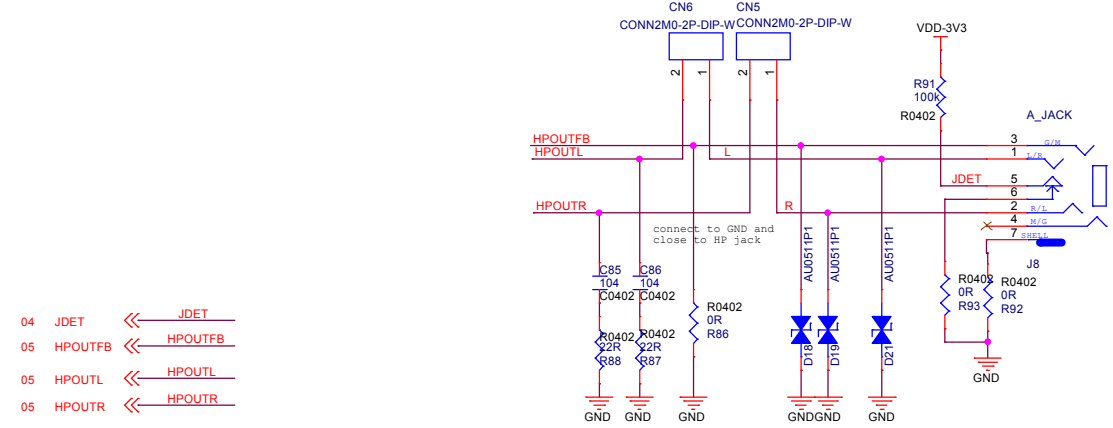


SPEAKER

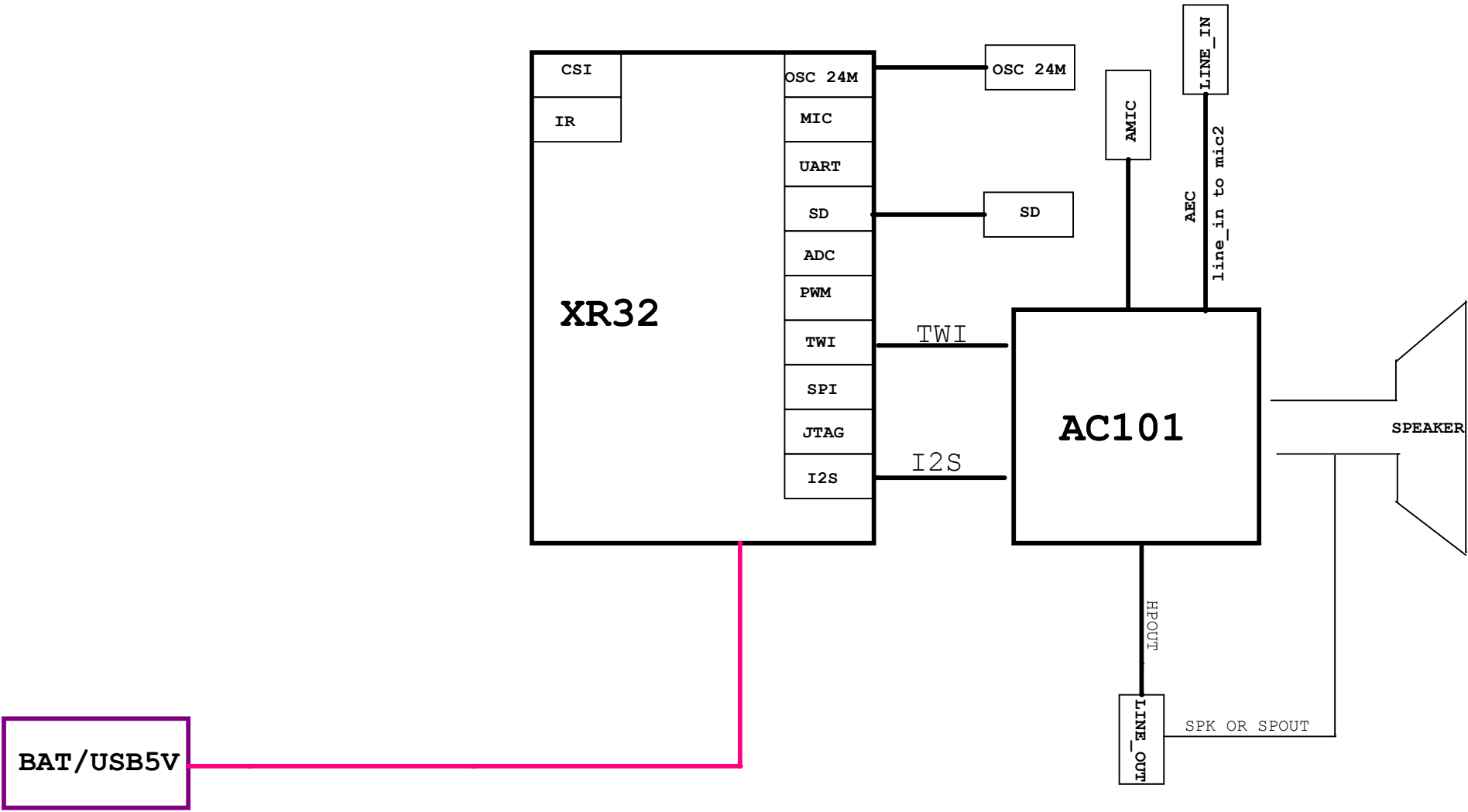


选择功放后 端输出, 用 CN3\CN4 ;
选择功放前 端输出, 用 CN7\CN8 ;
选择耳机 输出, 用 CN5\CN6 ;

HP JACK



BLOCK DIAGRAM



GPIO ASSIGNMENT

MAIN_BRD		
PIN	NAME	CFG
PB0	UART0_TX	1
PB1	UART0_RX	2/4
PB2	SPI0_WP	2/5
PB3	SPI0_HD	2/5
PB4	SPI0_MOSI	2/5
PB5	SPI0_MISO	2/5
PB6	SPI0_CS	2/5
PB7	SPI0_CLK	2/5
PB13	I/O	
PB15	I/O	

MAIN_BRD		
PIN	NAME	CFG
PA0	SD-CMD	2/4
PA1	SD-DATA0	2/4
PA2	SD-CLK	2/4
PA3	SD-DET	2/4
PA4	I/O	
PA5	I/O	
PA6	I/O	
PA7	I/O	
PA8	POWER_CTL	5
PA9	ADC	2
PA10	LED	3
PA11	LED	3
PA12	I2S_MCLK	2
PA13	I2S_BCLK	2
PA14	I2S_DI	2
PA15	I2S_DO	2
PA16	I2S_LRCLK	2
PA17	TWI0_SCL	2
PA18	TWI0_SDA	2
PA19	I/O	
PA20	SPK_CTL	2
PA21	I/O	
PA22	I/O	