

# REVISION HISTORY

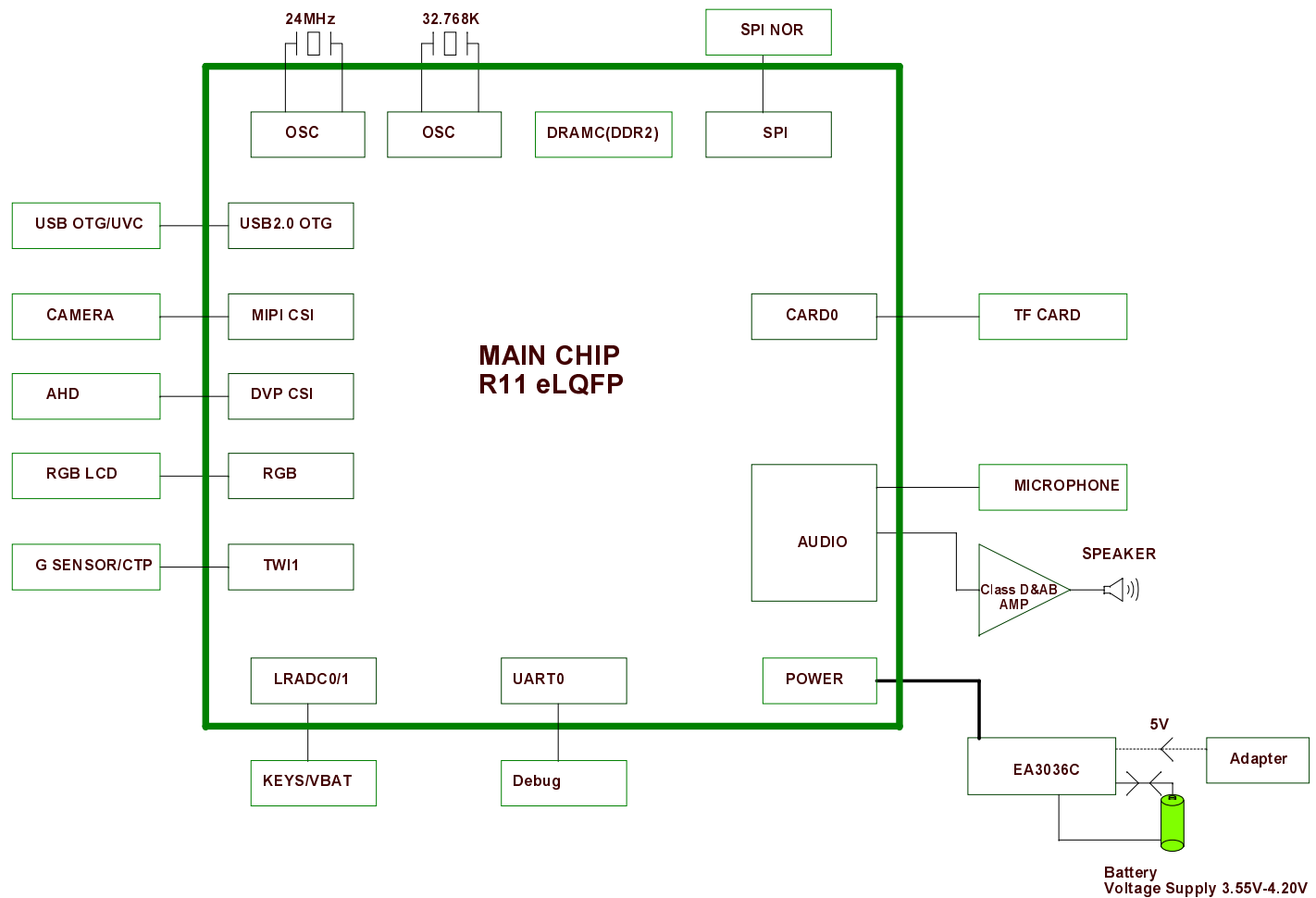
## Schematics Index:

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P05: CPU  
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P07: CAMERA-MIPI  
P08: SPI NOR/TF Card  
P09: SPK/MICHEADPHONE  
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P12: NEXTCHIP N3

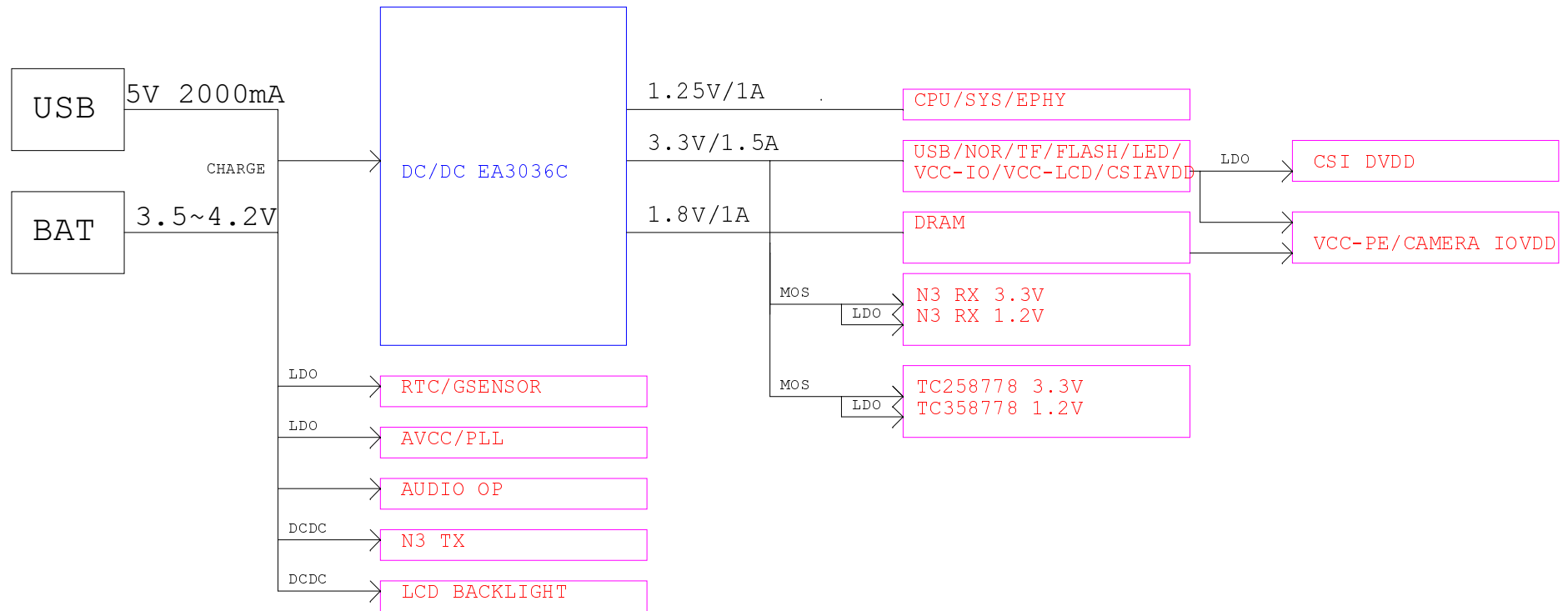
| Revision | Description | Date       | Drawn  | Checked |
|----------|-------------|------------|--------|---------|
| Ver 0.1  | Initial     | 2017-05-05 | HW DZH |         |

|                                                                                       |                                      |                               |     |
|---------------------------------------------------------------------------------------|--------------------------------------|-------------------------------|-----|
|  | <b>AllWinner Technology Co., Ltd</b> |                               |     |
|                                                                                       | Design Name                          |                               |     |
|                                                                                       | R11_STD_V1_0                         |                               |     |
|                                                                                       | Size<br>A3                           | Page Name<br>REVISION HISTORY | Rev |
| Date: Monday, December 04, 2017 Sheet 1 of 9                                          |                                      |                               |     |

# BLOCK DIAGRAM



# POWER TREE



# GPIO ASSIGNMENT

| PIN  | Define      | CFG | Function              |
|------|-------------|-----|-----------------------|
| PB0  | CTP-WAKE    | 1   | CTP                   |
| PB1  | CTP-INT     | 0   |                       |
| PB2  | LCD-BL-EN1  | 1   |                       |
| PB3  | LCD-PWR-EN1 | 1   | LCD                   |
| PB4  | PWM0        | 2   |                       |
| PB5  | PA-SHDN     | 1   | AUDIO                 |
| PB6  | TWI0-SCK    | 2   | Gsensor/CTP/N3/358778 |
| PB7  | TWI0-SDA    | 2   |                       |
| PB8  | UART0_TX    | 2   | DEBUG                 |
| PB9  | UART0_RX    | 2   |                       |
| PB10 | SD-DET      | 1   | TF                    |
|      |             |     |                       |
|      |             |     |                       |
|      |             |     |                       |

| PIN | Define    | CFG | Function     |
|-----|-----------|-----|--------------|
| PC0 | SPI0_MISO | 3   | NOR/<br>NAND |
| PC1 | SPI0_CLK  | 3   |              |
| PC2 | SPI0_CS   | 3   |              |
| PC3 | SPI0_MOSI | 3   |              |

| PIN | Define   | CFG | Function |
|-----|----------|-----|----------|
| PF0 | SDC0-D1  | 2   | TF CARD  |
| PF1 | SDC0-D0  | 2   |          |
| PF2 | SDC0-CLK | 2   |          |
| PF3 | SDC0-CMD | 2   |          |
| PF4 | SDC0-D3  | 2   |          |
| PF5 | SDC0-D2  | 2   |          |

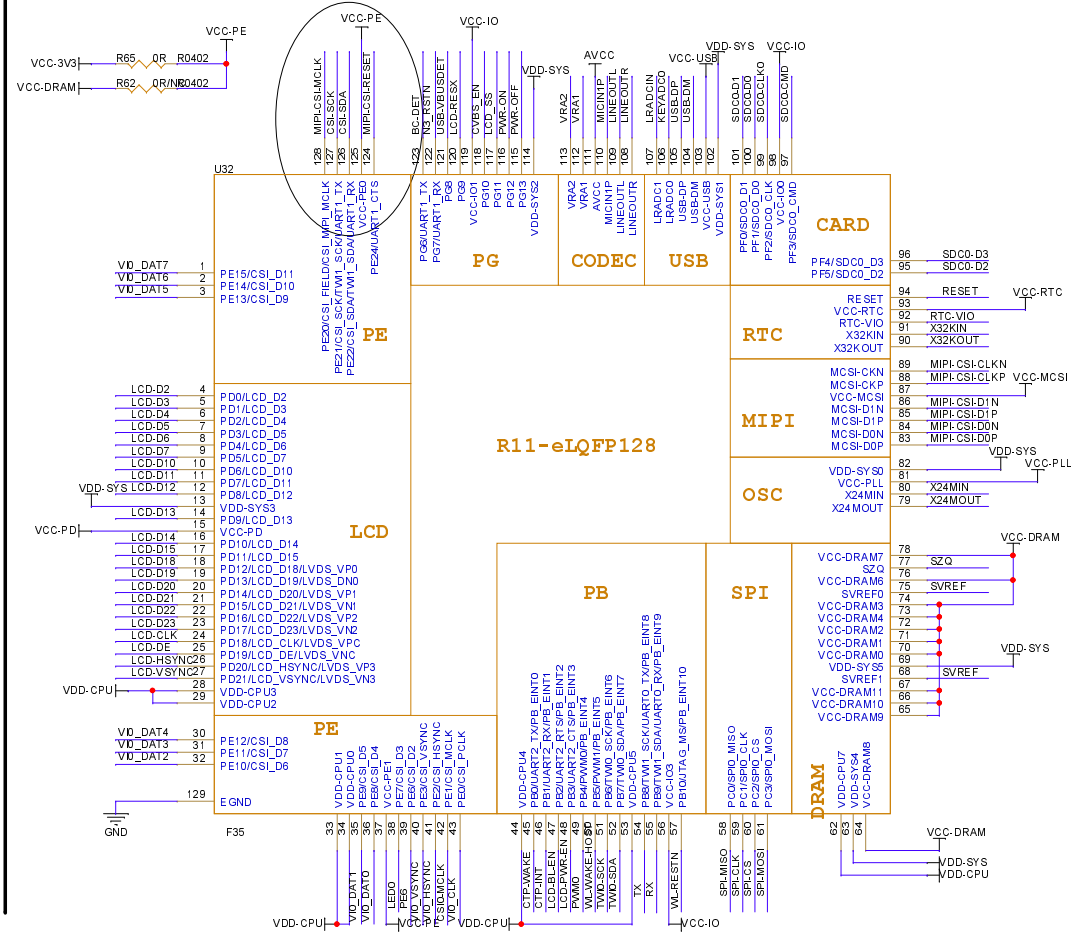
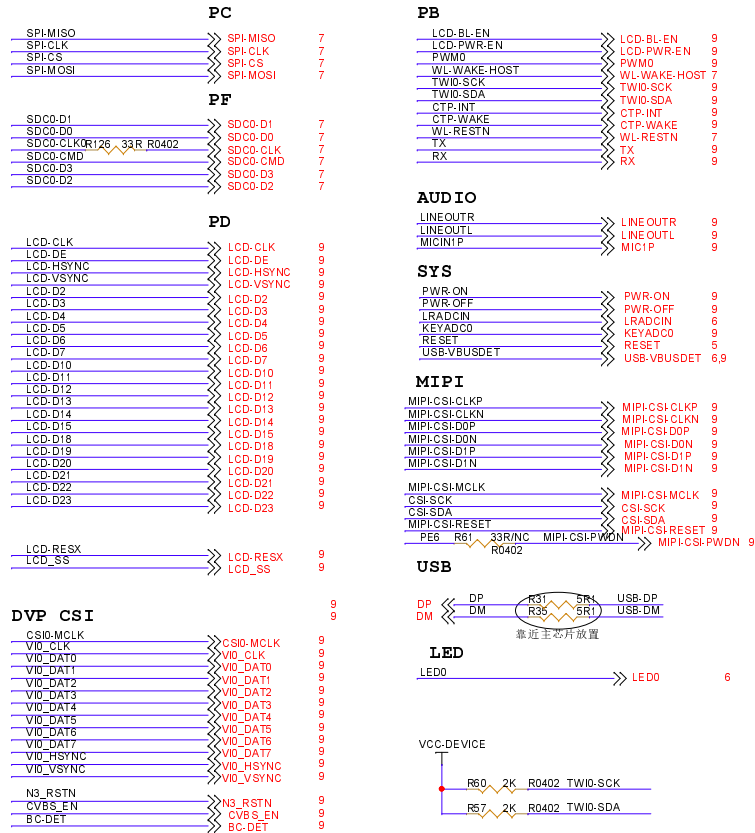
| PIN  | Define    | CFG | Function |
|------|-----------|-----|----------|
| PD0  | LCD_D2    | 3   | LCD      |
| PD1  | LCD_D3    | 3   |          |
| PD2  | LCD_D4    | 3   |          |
| PD3  | LCD_D5    | 3   |          |
| PD4  | LCD_D6    | 3   |          |
| PD5  | LCD_D7    | 3   |          |
| PD6  | LCD_D10   | 3   |          |
| PD7  | LCD_D11   | 3   |          |
| PD8  | LCD_D12   | 3   |          |
| PD9  | LCD_D13   | 3   |          |
| PD10 | LCD_D14   | 3   |          |
| PD11 | LCD_D15   | 3   |          |
| PD12 | LCD_D18   | 3   |          |
| PD13 | LCD_D19   | 3   |          |
| PD14 | LCD_D20   | 3   |          |
| PD15 | LCD_D21   | 3   |          |
| PD16 | LCD_D22   | 3   |          |
| PD17 | LCD_D23   | 3   |          |
| PD18 | LCD_CLK   | 3   |          |
| PD19 | LCD_DE    | 3   |          |
| PD20 | LCD_HYSNC | 3   |          |
| PD21 | LCD_VYSNC | 3   |          |

| PIN  | Define      | CFG | Function |
|------|-------------|-----|----------|
| PG6  | BC_DET      | 1   | GPIO     |
| PG7  | N3_RSTN     | 1   |          |
| PG8  | USB-VBUSDET | 0   |          |
| PG9  | LCD-RESX    | 1   |          |
| PG10 | CVBS_EN     | 0   |          |
| PG11 | LCD_SS      | 0   |          |
| PG12 | PWR-ON      | 0   |          |
| PG13 | PWR-OFF     | 0   |          |

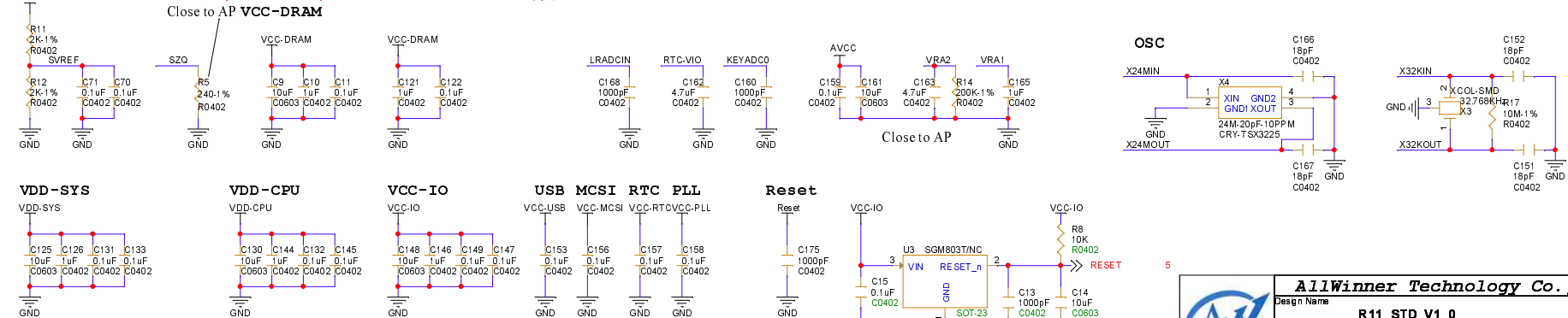
| PIN  | Define     | CFG | Function |
|------|------------|-----|----------|
| PE0  | CSI_PCLK   | 3   | LCD      |
| PE1  | CSI_MCLK   | 3   |          |
| PE2  | CSI_HSYNC  | 3   |          |
| PE3  | CSI_VSYNC  | 3   |          |
|      |            |     |          |
| PE6  | NC         | 3   |          |
| PE7  | LED0       | 3   |          |
| PE8  | CSI_DAT0   | 3   |          |
| PE9  | CSI_DAT1   | 3   |          |
| PE10 | CSI_DAT2   | 3   |          |
| PE11 | CSI_DAT3   | 3   |          |
| PE12 | CSI_DAT4   | 3   |          |
| PE13 | CSI_DAT5   | 3   |          |
| PE14 | CSI_DAT6   | 3   |          |
| PE15 | CSI_DAT7   | 3   |          |
|      |            | 3   |          |
|      |            | 3   |          |
|      |            | 3   |          |
|      |            | 3   |          |
| PE20 | MIPI-MCLK  | 3   |          |
| PE21 | CSI-SCK    | 2   |          |
| PE22 | CSI-SDA    | 2   |          |
| PE23 | MIPI-PWDN  | 3   |          |
| PE24 | MIPI-RESET | 3   |          |



# CPU



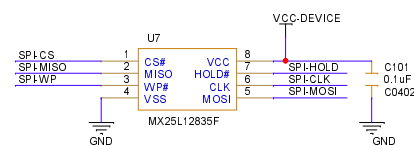
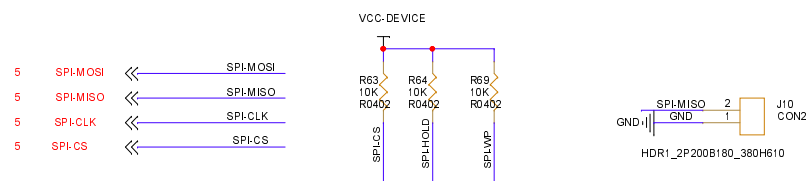
注：VCC-DRAM/VCC-SYS/VCC-CPU的电容均匀放置于主控的各引脚。



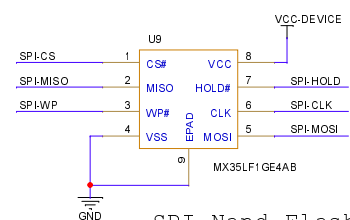
[illegible]

## Flash

NOR

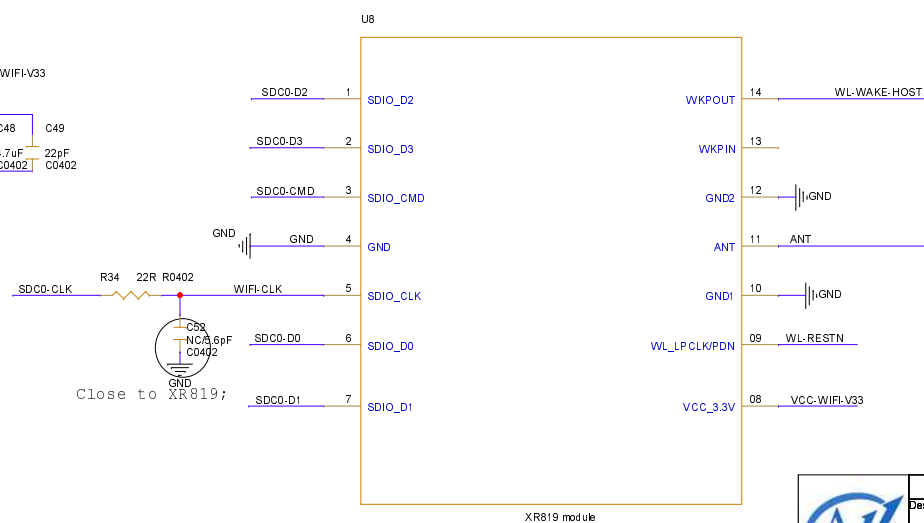
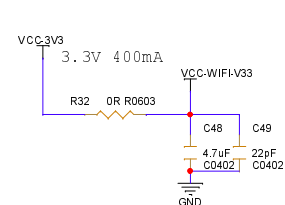
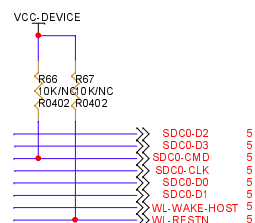


SPI Nor Flash

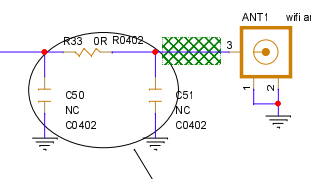


SPI Nand Flash

**wifi:XR819**



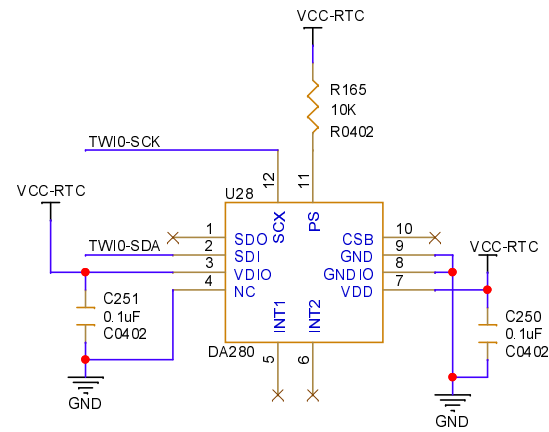
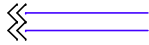
XR819 module



### PI Matching Circuit

# GSENSOR

5,9 TWI0-SCK  
5,9 TWI0-SDA



OPTION



**AllWinner Technology Co., Ltd**

Design Name

**R11\_STD\_V1\_0**

Size  
A4

Page Name

**GSENSOR**

Rev

Date: Monday, December 04, 2017

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